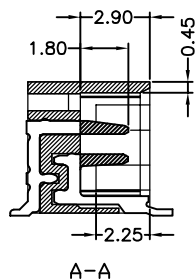
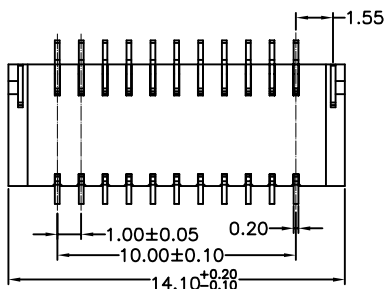


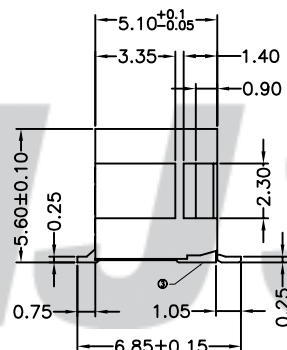
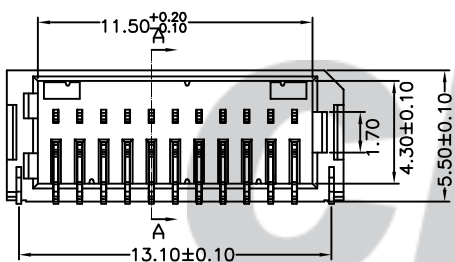
RoHS Compliant

成品图



SPECIFICATION

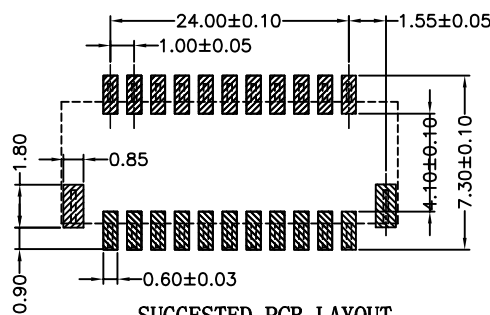
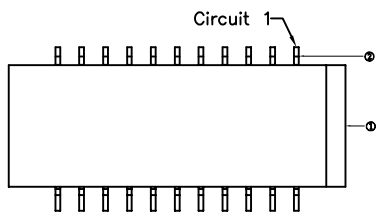
- 1 Contact resistance: $\leq 20m\Omega$
- 2 Insulation resistance: $\geq 100M\Omega$
- 3 Rated voltage: 50 V AC DC
- 4 Rated current: 1.0 A AC DC
- 5 Withstand Voltage: 500V AC/minute
- 6 Temperature Range: $-25^{\circ}C \sim +85^{\circ}C$



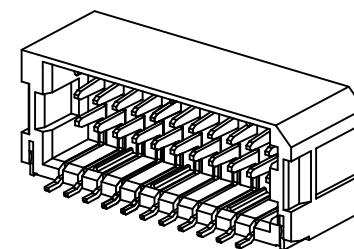
ORDER INFORMATION:

A1001AWRR-211-F2KC-R

PART No. _____
 No. OF CIRCUITS: _____
 2X11 _____
 PLASTIC MATERIAL: _____
 F2K=LCP _____
 PACKAGING: R=REEL
 PLATING: C=GOLD



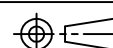
SUGGESTED PCB LAYOUT
(COMPONENT SIDE)



B/0	2016.06.12	NEW RELEASE	
REV.	DATE	DESCRIPTION	APPD.

③	Solder Tab	2	Phosphor Bronze	Mattn Sn
②	Contacts	22	Phosphor Bronze	Gold+Mattn Sn
①	Insulator	1	LCP (UL94V-0)	Black
NO.	DESCRIPTION	QTY	MATERIAL	PLATING/COLOR

Tolerance
 X.X ±0.30
 X.XX ±0.20
 X.XXX ±0.10
 Angle ±3.0°
 UNIT: mm

Model Name: 1.00mm pitch 90° Wafer-SMT TYPE
 Model No: A1001AWRR-211-F2KC-R

 SCALE: FULL REVISION: B/0
CNJST 浙江捷仕泰电子有限公司
 ZHEJIANG JIESHITAI ELECTRONIC CO., LTD

Drawn: 张育斌 2016.06.12
 Checked: 林旭芝
 Approved: 刘祚杰
 DWG NO. CNJST/GCCP-0217