

RoHS Compliant

成品图

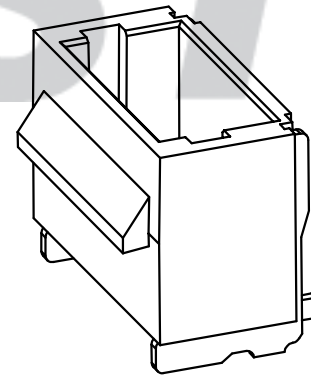
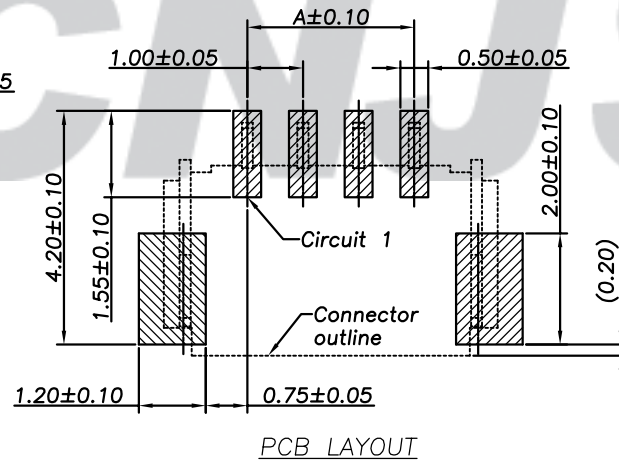
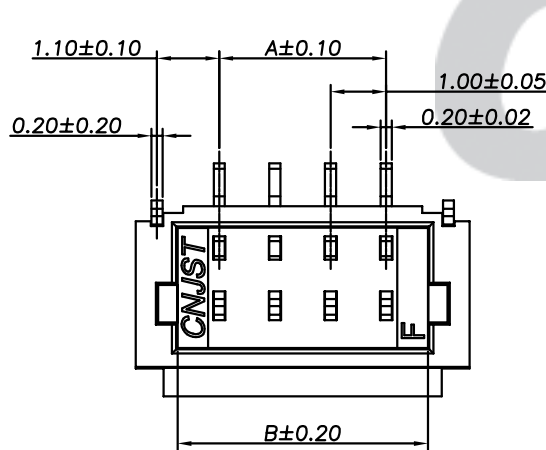
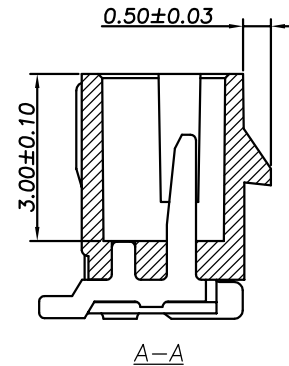
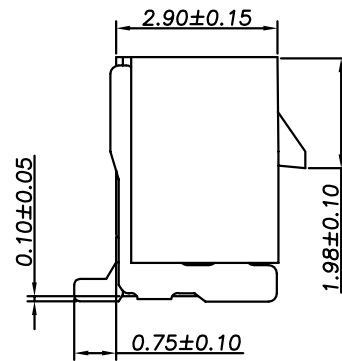
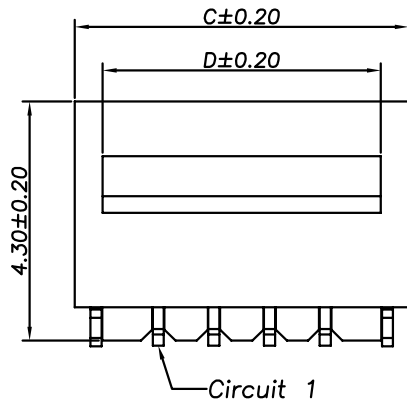
SPECIFICATION

- 1 Contact resistance: $\leq 20m\Omega$
- 2 Insulation resistance: $\geq 100M\Omega$
- 3 Rated voltage: 50 V AC DC
- 4 Rated current: 1.0 A AC DC
- 5 Withstand Voltage: 500V AC/minute
- 6 Temperature Range: $-25^{\circ}C \sim +85^{\circ}C$

ORDER INFORMATION:

A1001AWVR--F2MA-R-X**

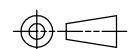
PART No. _____
 No. OF CIRCUITS: _____ PACKAGING: _____
 02~20 R=Reel
 PLASTIC MATERIAL: _____ PLATING: _____
 F2M=LCP(Nature) A=Tin-plated



Circuits	Dimensions			
	A	B	C	D
02	1.00	2.50	4.00	3.00
03	2.00	3.50	5.00	4.00
04	3.00	4.50	6.00	5.00
05	4.00	5.50	7.00	6.00
06	5.00	6.50	8.00	
07	6.00	7.50	9.00	
08	7.00	8.50	10.00	
09	8.00	9.50	11.00	
10	9.00	10.50	12.00	8.00
11	10.00	11.50	13.00	
12	11.00	12.50	14.00	
13	12.00	13.50	15.00	
14	13.00	14.50	16.00	
15	14.00	15.50	17.00	
16	15.00	16.50	18.00	
17	16.00	17.50	19.00	
18	17.00	18.50	20.00	
19	18.00	19.50	21.00	
20	19.00	20.50	22.00	

Unit: mm

专利产品

B/02016.04.12 NEW RELEASE				③ Solder Tabs	2	Phosphor Bronze	Tin-plated	Tolerance X.X ±0.30 X.XX ±0.20 X.XXX ±0.10 Angle ±3.0°	Model Name: 1.00mm pitch 180° WAFER-SMT TYPE Model No: A1001AWVR-**-F2MA-R-X	 SCALE: FULL REVISION: B/0	Drawn: 张育斌 2016.04.12
REV.	DATE	DESCRIPTION	APPD.	② Contacts	2~20	Phosphor Bronze	Tin-plated				Checked: 林旭芝
① Insulator				① Insulator	1	LCP (UL94V-0)	Nature	UNIT: mm	CNJST 浙江捷仕泰电子有限公司 ZHEJIANG JIESHITAI ELECTRONIC CO., LTD	Approved: 刘祚杰 DWG NO. CNJST/GCCP-0282	
NO.	DESCRIPTION	QTY	MATERIAL	PLATING/COLOR							