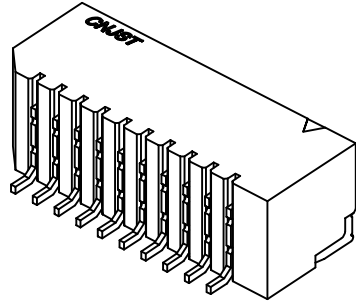
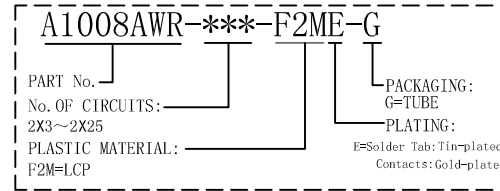


RoHS Compliant

成品图

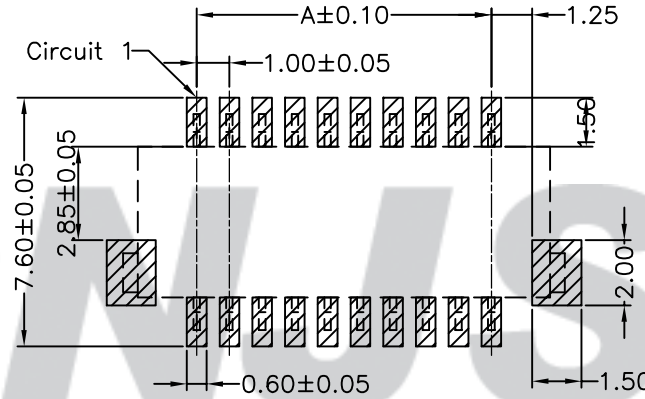
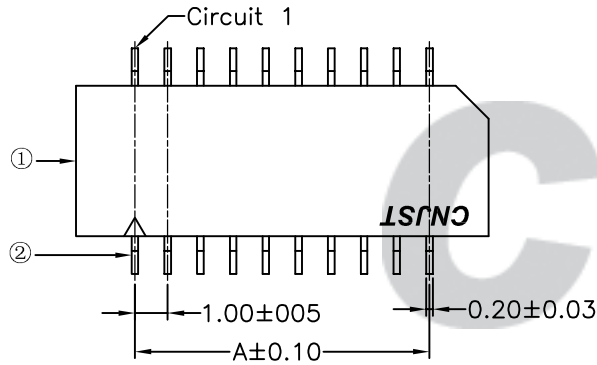


ORDER INFORMATION:



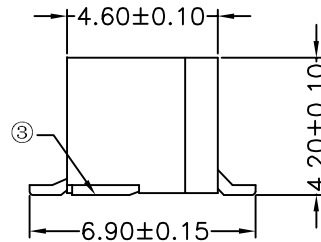
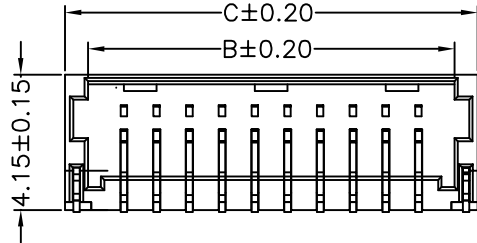
SPECIFICATION

- 1 Contact resistance:  $\leq 20m\Omega$
- 2 Insulation resistance:  $\geq 100M\Omega$
- 3 Rated voltage: 50 V AC DC
- 4 Rated current: 1.0 A AC DC
- 5 Withstand Voltage: 250V AC/minute
- 6 Temperature Range:  $-25^{\circ}C \sim +85^{\circ}C$



SUGGESTED PCB LAYOUT  
(COMPONENT SIDE)

Circuits	Dimensions (mm)		
	A	B	C
2X03	2.00	4.20	5.60
2X04	3.00	5.20	6.60
2X05	4.00	6.20	7.60
2X06	5.00	7.20	8.60
2X07	6.00	8.20	9.60
2X08	7.00	9.20	10.60
2X09	8.00	10.20	11.60
2X10	9.00	11.20	12.60
2X11	10.00	12.20	13.60
2X12	11.00	13.20	14.60
2X13	12.00	14.20	15.60
2X14	13.00	25.20	16.60
2X15	14.00	16.20	17.60
2X16	15.00	17.20	18.60
2X17	16.00	18.20	19.60
2X18	17.00	19.20	20.60
2X19	18.00	20.20	21.60
2X20	19.00	21.20	22.60
2X21	20.00	22.20	23.60
2X22	21.00	23.20	24.60
2X23	22.00	24.20	25.60
2X24	23.00	25.20	26.60
2X25	24.00	26.20	27.60



Tolerance X.X ±0.30 X.XX ±0.20 X.XXX ±0.10 Angle ±3.0°	Model Name: 1.00mm pitch 90° Wafer-SMT TYPE		Drawn: 张育斌 2016.06.12
	Model No: A1008AWR-***-F2ME-G		
UNIT: mm	SCALE: FULL REVISION: B/0	Checked: 林旭芝	
		Approved: 刘祚杰	
<b>CNJST</b> 浙江捷仕泰电子有限公司 ZHEJIANG JIESHITAI ELECTRONIC CO., LTD		DWG NO. CNJST/GCCP-0216	

REV.	DATE	DESCRIPTION	APPD.	NO.	DESCRIPTION	QTY	MATERIAL	PLATING/COLOR
B/0	2016.04.12	NEW RELEASE		③	Solder Tab	2	Phosphor Bronze	Tin-plated
				②	Contacts	06~50	Phosphor Bronze	Gold-plated
				①	Insulator	1	LCP (UL94V-0)	Nature