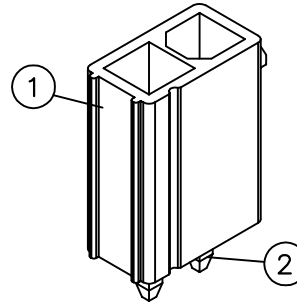
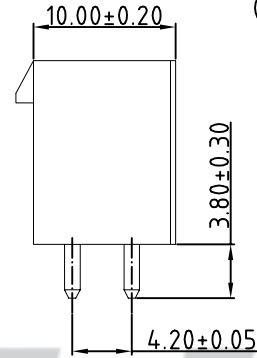
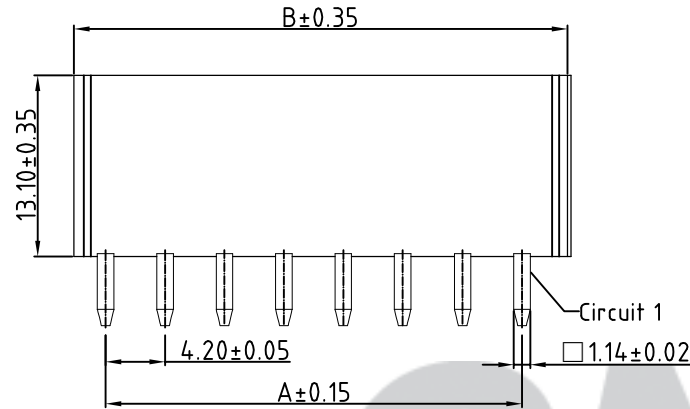


RoHS Compliant

成品图



SPECIFICATION

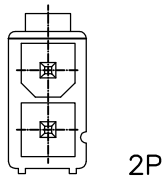
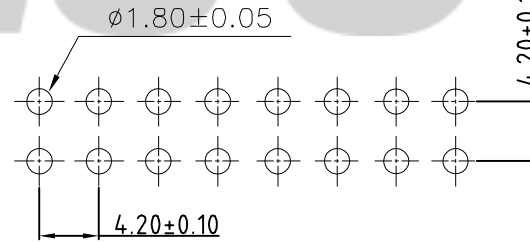
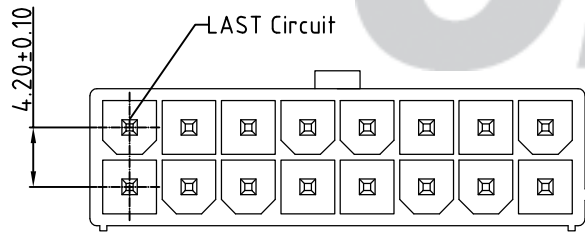
- 1 Contact resistance: $\leq 15m\Omega$
- 2 Insulation resistance: $\geq 1000M\Omega$
- 3 Rated voltage: 300 V AC DC
- 4 Rated current: 9.0 A AC DC
- 5 Withstand Voltage: 1500V AC/minute
- 6 Temperature Range: $-25^{\circ}C \sim +85^{\circ}C$

ORDER INFORMATION:

A4201A-***-A0TA-P			
PART No.			
No. OF CIRCUITS:			
201~212			
PLASTIC MATERIAL:		PACKAGING:	
A0T=PA66 (TRANSPARENT)		P=PE	
		PLATING:	
		A=Tin-Plated	

CNJST

Circuits	Dimensions	
	A	B
201(2*1)	---	5.40
202(2*2)	4.20	9.60
203(2*3)	8.40	13.80
204(2*4)	12.60	18.00
205(2*5)	16.80	22.20
206(2*6)	21.00	26.40
207(2*7)	25.20	30.60
208(2*8)	29.40	34.80
209(2*9)	33.60	39.00
210(2*10)	37.80	43.20
211(2*11)	42.00	47.40
212(2*12)	46.20	51.60



Tolerance X.X ±0.30 X.XX ±0.20 X.XXX ±0.10 Angle ±3.0°	Model Name: 4.20mm pitch 180° WAFER-DIP TYPE	Drawn: 张育斌 2016.05.19
	Model No: A4201A-***-A0TA-P	
UNIT: mm	SCALE: FULL	REVISION: B/0
	CNJST 浙江捷仕泰电子有限公司 ZHEJIANG JIESHITAI ELECTRONIC CO., LTD	
		Approved: 刘祚杰
		DWG NO. CNJST/GCCP-0368

REV.	DATE	DESCRIPTION	APPD.	NO.	DESCRIPTION	QTY	MATERIAL	PLATING/COLOR
				②	Contacts	02~24	Barss (H65)	Tin-Plated
				①	Insulator	1	PA66 (UL94V-0)	TRANSPARENT
B/0	2016.04.12	NEW RELEASE						