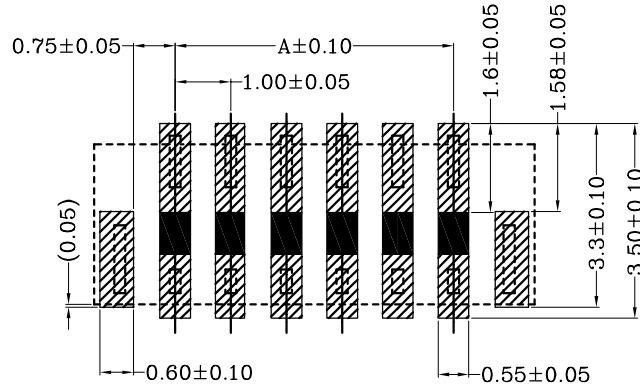
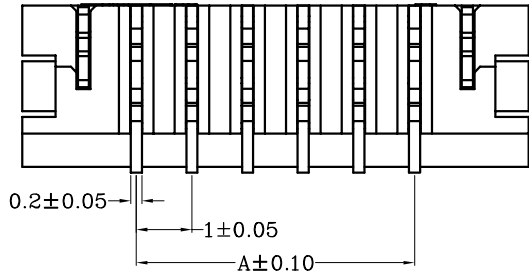


RoHS Compliant

成品图



PCB LAYOUT

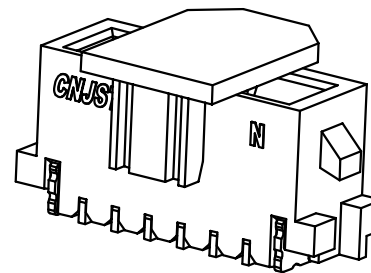
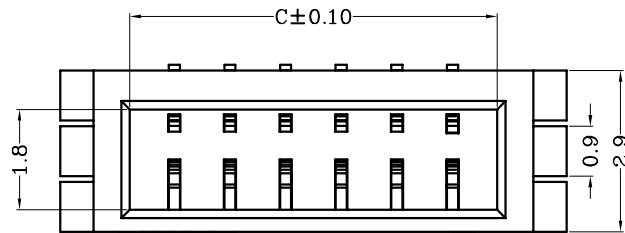
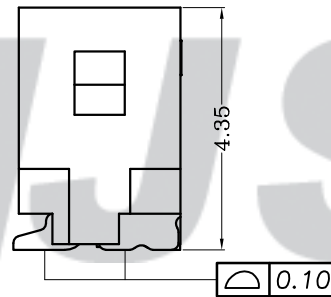
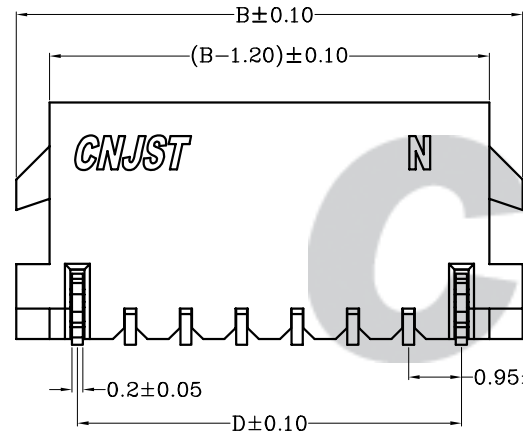
SPECIFICATION

- 1 Contact resistance: $\leq 20m\Omega$
- 2 Insulation resistance: $\geq 100M\Omega$
- 3 Rated voltage: 125 V AC DC
- 4 Rated current: 1.0 A AC DC
- 5 Withstand Voltage: 500V AC/minute
- 6 Temperature Range: $-25^{\circ}C \sim +85^{\circ}C$

ORDER INFORMATION:

A1018AWV--C1MB-R-X**

PART No: _____ X=Cap
 No. OF CIRCUITS: _____ PACKAGING: _____
 02~15 R=REEL
 PLASTIC MATERIAL: _____ PLATING: _____
 C1M=FR52 (Nature) B=Matte Sd



| Circuits | Dimensions | | | |
|----------|------------|-------|-------|-------|
| | A | B | C | D |
| 02 | 1.00 | 5.10 | 2.50 | 3.00 |
| 03 | 2.00 | 6.10 | 3.50 | 4.00 |
| 04 | 3.00 | 7.10 | 4.50 | 5.00 |
| 05 | 4.00 | 8.10 | 5.50 | 6.00 |
| 06 | 5.00 | 9.10 | 6.50 | 7.00 |
| 07 | 6.00 | 10.10 | 7.50 | 8.00 |
| 08 | 7.00 | 11.10 | 8.50 | 9.00 |
| 09 | 8.00 | 12.10 | 9.50 | 10.00 |
| 10 | 9.00 | 13.10 | 10.50 | 11.00 |
| 11 | 10.00 | 14.10 | 11.50 | 12.00 |
| 12 | 11.00 | 15.10 | 12.50 | 13.00 |
| 13 | 12.00 | 16.10 | 13.50 | 14.00 |
| 14 | 13.00 | 17.10 | 14.50 | 15.00 |
| 15 | 14.00 | 18.10 | 15.50 | 16.00 |

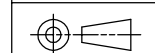
Unit: mm

| REV. | DATE | DESCRIPTION | APPD. |
|------|------------|-------------|-------|
| B/0 | 2016.04.12 | NEW RELEASE | |

| NO. | DESCRIPTION | QTY | MATERIAL | PLATING/COLOR |
|-----|-------------|------|-----------------|---------------|
| ④ | Cap | 1 | LCP (UL94V-0) | Nature |
| ③ | Solder Tab | 2 | Phosphor Bronze | Mattn Sn |
| ② | Material | 2~15 | Phosphor Bronze | Mattn Sn |
| ① | Insulator | 1 | FR52 (UL94V-0) | Nature |

Tolerance
 X.X ±0.30
 X.XX ±0.20
 X.XXX ±0.10
 Angle ±3.0°

Model Name: 1.00mm Pitch 90° Single Row Wafer With Lock-SMT TYPE
 Model No: A1018AWV-**-C1MB-R-X



SCALE: FULL REVISION: B/0

CNJST 浙江捷仕泰电子有限公司
 ZHEJIANG JIESHITAI ELECTRONIC CO., LTD

Drawn: 张育斌 2016.05.03

Checked: 林旭芝

Approved: 刘祚杰

DWG NO. CNJST/GCCP-0408